PRODUCT SPECIFICATION

2.4" IPS LCD Module with SPI Interface DT024ETFT-IPS, DT024ETFT-IPS-SHB



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Revision History

REV	DESCRIPTION	DATE	APPR
1.0	Initial release	14 AUG 2023	PRW



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1 Overview

The **DT024ETFT-IPS** and **DT024ETFT-IPS-SHB** are 2.4" color IPS LCD modules, each composed of a LCD panel, display drivers, FPC display cable with SPI interface, and adjustable LED backlight unit. The display's active area has a resolution of 240 x 320 pixels. The DT024ETFT-IPS-SHB, with twice the number of backlight LEDs, is available as a "super high-bright" alternative to the DT024ETFT-IPS.

1.1 Applications

- Video Systems
- Mobile Systems
- Wearable devices

1.2 Features

Size
 2.4 Inches

Resolution
 240 (RGB) x 320 Pixels

Type
 IPS, Normally Black, Transmissive

Interface 3-Line SPI, 4-Line SPI

Module Dimensions

DT024ETFT-IPS 42.52 mm (W) x 59.86 mm (L) x 2.40 mm (H)
DT024ETFT-IPS-SHB 42.52 mm (W) x 59.86 mm (L) x 2.50 mm (H)

Active Area 36.72 mm (W) x 48.96 mm (L)

• Pixel Pitch 0.153 mm (W) x 0.153 mm (L)

Viewing Direction
 All

Backlight Type
 LED, White

• LCD Driver ILI9341

1.3 Acronyms

FPC
 LCD
 Liquid Crystal Display
 LED
 Light Emitting Diode
 RGB
 Red-Green-Blue

SPI Serial-Peripheral Interface



2 Pin Descriptions

	LCD INTERFACE ¹				
PIN	NAME	TYPE	DESCRIPTION		
1	GND	PWR	Ground		
2	VDDIO	PWR	Power supply, logic		
3	VDD	PWR	Power supply, analog		
4	CS	I	Chip select (active low)		
5	RESET	I	Display reset, (active low)		
6	SDIO	I/O	Serial input/output		
7	GND	PWR	Ground		
8	D/CX	I	Data/Command selection (0: Command, 1: Data)		
9	SCL	I	Serial clock		
10	IM1	I	See MCU Parallel Interface Mode Selection table		
11	IM0	I	See MCU Parallel Interface Mode Selection table		
12	TE	0	Tearing effect (active high). Leave open when not in use. ²		
13	LED-A	PWR	LED backlight, anode		
14	LED-K	PWR	LED backlight, cathode		
15	LED-K	PWR	LED backlight, cathode		

2.1 MCU Parallel Interface Mode

PARALLEL INTERFACE MODE SELECTION				
IM1	IM0	MODE		
0	0	8-bit		
0	1	16-bit		
1	0	9-bit		
1	1	18-bit		

² See ILI9163 datasheet for details



¹ Recommended mating connector: 5051101592 (or equivalent)



3 Specifications

3.1 Absolute Maximum Ratings³

ELECTRICAL							
PARAMETER MIN MAX UNITS							
Supply Voltage, Analog	V_{DD}	-0.3	4.6	V			
Supply Voltage, Logic	V _{DDIO}	-0.3	4.6	V			
Logic Input Voltage	V _{IN}	-0.3	V _{DDIO} + 0.3	V			
Logic Output Voltage	V _{OUT}	-0.3	V _{DDIO} + 0.3	V			
Forward Current, Backlight	I _F	30	60	mA			

ENVIRONMENTAL						
PARAMETER	MIN	MAX	UNITS			
Operating Temperature	T _{OP}	-20	70	°C		
Storage Temperature	T _{ST}	-30	80	°C		

3.2 Electrical Characteristics⁴

POWER							
PARAMETER MIN TYP MAX UNITS							
Supply Voltage, Analog	V_{DD}	2.5	2.8	3.3	V		
Supply Voltage, Logic	V_{DDIO}	2.5	2.8	3.3	V		
Supply Current	I _{DD}	_	10	15	mA		

LOGIC								
PARAMETER MIN TYP MAX UNITS								
Logic Input, High	V _{IH}	0.7 x V _{DDIO}	_	V_{DDIO}	V			
Logic Input, Low	V _{IL}	GND	_	$0.3 \times V_{DDIO}$	V			
Logic Output, High	V _{OH}	0.8 * V _{DDIO}	_	V_{DDIO}	V			
Logic Output, Low	V _{OL}	GND	_	0.2 x V _{DDIO}	V			



 $^{^3}$ Operation outside of the maximum ratings listed below may result in permanent damage to the LCD. 4 $T_{\rm A}$ = $25^{\circ}{\rm C}$



LED BACKLIGHT						
	PARAMETER MIN TYP MAX UNITS					
Forward Current	I _F		30	40	60	mA
Forward Voltage	V _F	DT024ETFT-IPS⁵	8.4	_	9.9	V
		DT024ETFT-IPS-SHB ⁶	16.8	_	19.8	
LED Lifetime ⁷			_	30,000	_	Hrs

3.2.1 LED Backlight Circuit

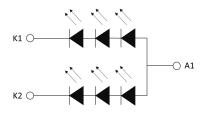


Figure 1: DT024ETFT-IPS Backlight $2 \times 3 = 6 \text{ LEDs}, I_F = 40 \text{ mA}$

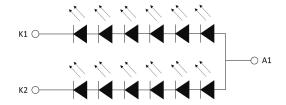


Figure 2: DT024ETFT-IPS-SHB Backlight $2 \times 6 = 12 \text{ LEDs}, I_F = 40 \text{ mA}$

⁵ DT024ETFT-IPS backlight power consumption: 594mW (Max.)

⁶ DT024ETFT-IPS-SHB backlight power consumption: 1.19W (Max.)

⁷ LED lifetime is defined as the amount of time it takes for brightness to decrease to 50% of its original value at T_A =25°C and I_F =40mA. LED lifetime may decrease if operating current, I_F , is larger than 40mA.



4 AC Timing Characteristics

4.1 3-Line Serial Interface

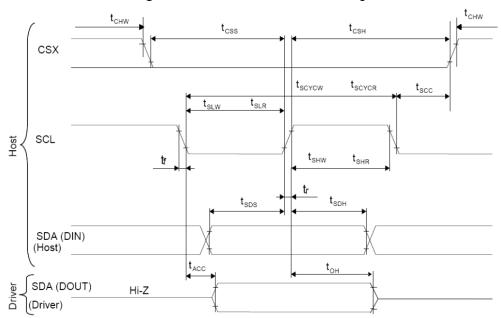


Figure 3: 3-Line Serial Interface Timing

	AC TIMING CHARACTERISTICS, 3-LINE SERIAL INTERFACE						
SIGNAL	PARAMETER	MIN	MAX	UNITS			
SCL	Serial clock cycle (write)	t _{scycw}	100	_	nS		
	SCL "H" pulse width (write)	t _{shw}	40	_	nS		
	SCL "L" pulse width (write)	t _{SLW}	40	_	nS		
	Serial clock cycle (read)	t _{scycr}	150	_	nS		
	SCL "H" pulse width (read)	t _{SHR}	60	_	nS		
	SCL "L" pulse width (read)	t _{SLR}	60	_	nS		
SDIO	Data setup time (write)	t _{SDS}	30	_	nS		
(Input)	Data hold time (write)	t _{SDH}	30	_	nS		
SDIO	Access time (read)	t _{ACC}	10	_	nS		
(Output) ⁸	Output disable time (read)	t _{OH}	10	50	nS		
CS	SCL - CSX	t _{scc}	20	_	nS		
	CSX "H" pulse width	t _{chw}	40	_	nS		
	CSX SCL time	t _{css}	60	_	nS		
		t _{CSH}	65	_	nS		

 $^{^{8}}$ Maximum C_L = 30pF; Minimum C_L = 8pF





4.2 4-Line Serial Interface

CSX t_{css} t_{csh} t_{wc}/t_{rc} t_{wr}/t_{rdl} t_{dh} t_{dh}

Figure 4: 4-Line Serial Interface Timing

AC TIMING CHARACTERISTICS, 4-Line Serial Interface						
SIGNAL	PARAMETER		MIN	MAX	UNITS	
CS	Chip select time (write)	t _{CSS}	40	_	nS	
SCL	Chip select hold time (read)	t _{CSH}	40	_	nS	
	Serial clock cycle (write)	t _{wc}	100	_	nS	
	SCL "H" pulse width (write)	t _{wr}	40	_	nS	
	SCL "L" pulse width (write)	t _{WRL}	40	_	nS	
	Serial clock cycle (read)	t _{RC}	150	_	nS	
	SCL "H" pulse width (read)	t _{RDH}	60	_	nS	
	SCL "L" pulse width (read)	t _{RDL}	60	_	nS	
D/CX	D/CX setup time	t _{AS}	10	_		
	D/CX hold time (write/read)	t _{AH}	10	_		
SDIO	Data setup time (write)	t _{DS}	30	_	nS	
(Input)	Data hold time (write)	t _{DH}	30	_	nS	
SDIO	Access time (read)	t _{ACC}	10	_	nS	
(Output) ⁹	Output disable time (read)	t _{OD}	10	50	nS	

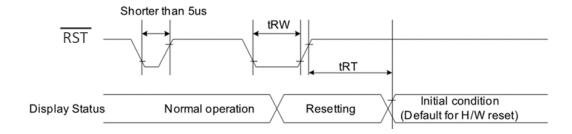
 $^{^{9}}$ Maximum C_L = 30pF; Minimum C_L = 8pF





4.4 Reset Timing

Figure 5: Reset Timing



RESET TIMING ¹⁰						
PARAMETER MIN MAX UNIT						
Reset pulse duration	t _{RW}	10	_	μS		
Reset cancel	t _{RT}	_	5	mS		
		_	120	mS		

¹⁰ Refer to ILI9341 driver datasheet for details





5 Optical Characteristics

OPTICAL CHARACTERISTICS ¹¹							
PARAMETER			MIN.	TYP.	MAX.	UNIT	
Contrast Ratio ^{12,13}		CR	600	800	_	_	
Response Time ¹⁴		T _{ON} / T _{OFF}		30	40	mS	
View Angles ^{15,16}		ΘТ	_	80	_	Degrees	
		ΘВ	_	80	_		
		ΘL	_	80	_		
		ΘR	_	80	_		
Chromaticity ¹⁷		X _{wht}	0.3050	0.3250	0.3450	_	
		Y _{WHT}	0.3404	0.3604	0.3804		
		X_{RED}	0.6351	0.6551	0.6751		
		Y _{RED}	0.3034	0.3234	0.3432		
		X_{GRN}	0.3282	0.3482	0.3682		
		Y _{GRN}	0.5828	0.6028	0.6228		
		X_{BLU}	0.0864	0.1064	0.1264		
		Y _{BLU}	0.0364	0.0564	0.0764		
Luminance ¹³	DT024ETFT-IPS	L	_	350	_	Cd/m ²	
	DT024ETFT-IPS-SHB		_	1000 (SHB)	_		
Uniformity ¹³		U	80	_	_	%	

¹¹ See Section 5.1, Figure 3
12 Viewing Angle (Θ) = 0°
13 See Section 5.1, Figure 7
14 See Section 5.1, Figure 4
15 Contrast Ratio (CR) ≥ 10
16 See Section 5.1, Figure 5
17 See Section 5.1, Figure 6





5.1 Figures

Figure 3: Optical Measurement System

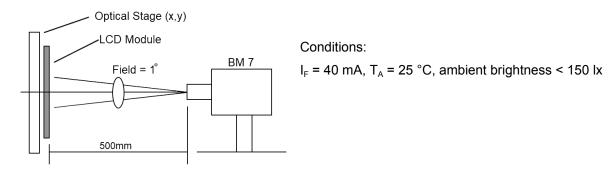
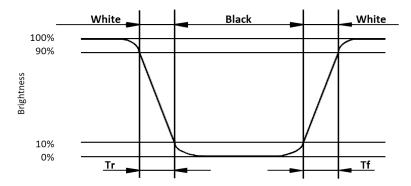


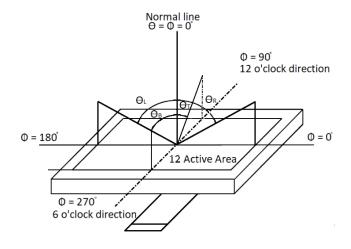
Figure 4: Response Times



Decay Time (TF) = Time required for display to transition from white to black

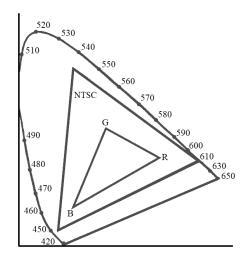
Rise Time (TR) = Time required for display to transition from black to white

Figure 5: Viewing Angles



Viewing angle is measured from center point of LCD

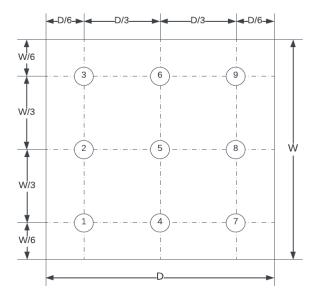
Figure 6: Chromaticity (CIE 1931)



Chromaticity = Area of \triangle_{RGB} / Area of \triangle_{NTSC}

* Color coordinates measured at center point of LCD

Figure 7: Luminance Uniformity



Luminance is defined as the brightness of all white pixels at the center of the display area at optimum contrast.

Uniformity is determined by measuring Luminance at 9 points and calculating Luminance_{MIN} / Luminance_{MAX}

 $Contrast Ratio = \frac{Surface Luminance_{WhitePixels}}{Surface Luminance_{BlackPixels}}$



6 Environmental/Reliability Tests

Judgment is based on inspection performed after testing, per criteria described in the Inspection Criteria table. 18

ITEM UNDER TEST	TEST CONDITION
High temp operation	T _A = 70 °C, 120 Hrs.
Low temp operation	T _A = -20 °C, 120 Hrs.
High temp storage	$T_{\rm S}$ = 80 °C, 120 Hrs.
Low temp storage	T _s = -30 °C, 120 Hrs.
High temp & high humidity storage	T _s = 60 °C, 120 Hrs., 90% RH
Thermal shock (non-operation)	-30 °C, 30 min → 80 °C, 30 min Change time: 5 min, 10 cycles
ESD (operation)	C = 150 pF, R = 330 Ω , 5 points/panel Air: ±8 KV, 5 times Contact: ±4 KV, 5 times
Vibration (non-operation)	Frequency range 10 - 55 Hz. Stroke: 1.5 mm Sweep 10 Hz → 55 Hz → 10 Hz 2 hours for each direction of X, Y, Z
Package drop test	Height 80 cm 1 corner, 3 edges, 6 surfaces

6.1 Inspection Criteria

All testing shall be judged based upon the criteria in this table.

INSPECTION ITEM	CRITERIA		
Appearance	No cracks on FPC No cracks on LCD panel		
Alignment of LCD panel	No bubbles in LCD panel No alignment defects in LCD active area		
Electrical current	Within device specifications		
Function / Display	No broken circuits, no short circuits No black lines No other display defects		

¹⁸ Functional test shall be conducted after 4 hours of storage at normal temperature and humidity, after LCD is removed from test





7 Precautions for Use of LCD Modules

7.1 Safety

Liquid crystal in LCD is poisonous. Do not put in mouth. If liquid crystal comes in contact with skin or clothes, wash it off immediately using soap and water.

7.2 Handling

- A. The LCD panel is made of plate glass. Do not subject the panel to mechanical shock or excessive force on its surface.
- B. In order to ensure reliability, do not hold product by flexible printed circuit (FPC) cable.
- C. Provide space so that panel does not come into contact with other components.
- D. To protect the product from external force, apply a covering lens (acrylic board or similar) and keep an appropriate gap between them.
- E. Transparent electrodes may be disconnected if the panel is used in an environment where dew condensation is present.
- F. Properties of semiconductor devices may be affected when exposed to light, possibly resulting in IC malfunctions. To prevent such malfunctions, design and mounting layout should be done in such a way that IC is not exposed to light in actual use.

7.3 Static electricity

- A. Ground soldering iron tips, tools and testers when they are in operation.
- B. Ground your body when handling the products.
- C. Power on the LCD module before applying the voltage to the input terminals.
- D. Do not apply voltage which exceeds the absolute maximum rating.
- E. Store the products in an anti-electrostatic bag or container.

7.4 Storage

- A. Store product in a dark place at $+25^{\circ}$ C $\pm 10^{\circ}$ C with low humidity (40% RH $\sim 60\%$ RH). Do not expose the display to sunlight or fluorescent light.
- B. Storage in a clean environment, free from dust, active gas, and solvent.

7.5 Cleaning

A. To clean the product, wipe with a soft cloth moistened with ethanol. Do not allow ethanol to get between upper film and bottom glass, as this may cause peeling issues and/or defective operation. Do not use any organic solvent or detergent other than ethanol.

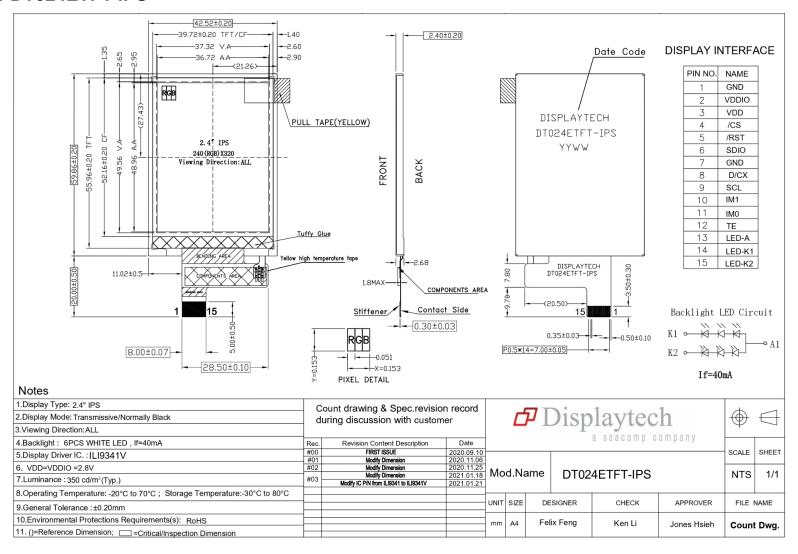
7.6 Cautions for installation and assembly

- A. Bezel edge must be positioned between Active area and Viewing area.
- B. For a stable display assembly, Displaytech recommends designing a support for the backside of the display.
- C. Do not display any fixed pattern for long periods of time. If a fixed pattern must be displayed, use a screen saver in order to avoid image persistence.



8 Mechanical Drawings

8.1 DT024ETFT-IPS





8.2 DT024ETFT-IPS-SHB

